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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/823,725	04/13/2004	Tsugihiko Matsuoka	09792909-5855	4792

26263 7590 11/02/2005

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EXAMINER

VU, HUNG K

ART UNIT PAPER NUMBER

2811

DATE MAILED: 11/02/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.

10/823,725

Applicant(s)

MATSUOKA, TSUGIHIRO

Examiner

Hung Vu

Art Unit

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☐ Responsive to communication(s) filed on \_\_\_\_.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-8 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-4, 7 and 8 is/are rejected.
- 7) ☒ Claim(s) 5 and 6 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

## Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
- ☒ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

## Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date \_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_.

## DETAILED ACTION

### *Specification*

1. The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

### *Claim Objections*

2. Claims 6 are objected to because of the following informalities: In claim 6, line 2, "said resistance" should be changed to "a resistance" for clarity. Appropriate correction is required.

### *Claim Rejections - 35 USC § 103*

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1-4 and 7-8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Takeoka et al. (PN 6,734,549).

Takeoka et al. discloses, as shown in Figures 8-27, a semiconductor device comprising:

a plurality of IC chips that are connected each other by wiring and disposed on a substrate;

wherein each IC chip is connected to outside the substrate by input wiring and output wiring;

a selector (46) is inserted in series into the wiring connection between the IC chips.

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Takeoka et al. does not disclose the selector is a transistor element. However, it would have been obvious to one of ordinary skill in the art at the time the invention was made to form the selector of Takeoka et al. as the transistor element or depletion mode FET since the selector and the transistor element perform the same switching function for selecting the signal inputs.

Regarding claim 2, Takeoka et al. does not disclose the transistor element serves as a damping resistor. However, it would have been obvious to one of ordinary skill in the art at the time the invention was made to form the selector of Takeoka et al. as the damping resistor in order to stabilize the input signals.

Regarding claim 3, Takeoka et al. does not disclose a termination resistance is formed on the wiring connecting between the IC chips. However, it would have been obvious to one of ordinary skill in the art at the time the invention was made to form the circuit of Takeoka et al. having a termination resistance formed on the wiring in order to control the current flow into the wiring.

Regarding claim 7, Takeoka et al. the substrate is a silicon substrate.

Regarding claim 8, Takeoka et al. discloses all of the claimed limitations except material of substrate. However, it would have been obvious to one of ordinary skill in the art at the time the invention was made to form the substrate of Takeoka et al. having the materials as that claimed by Applicant, since it has been held to be within the general skill of a worker in the art to select a

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known material on the basis of its suitability for the intended use as a matter of obvious design choice. *In re Leshin*, 125 USPQ 416.

#### ***Allowable Subject Matter***

4. Claims 5 and 6 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

5. The following is an examiner's statement of reasons for allowance:

Applicant's claims 5 and 6 are allowable over the references of record because none of these references disclose or can be combined to yield the claimed device having the transistor element and a resistance automatic compensation circuit being connected, as recited in claim 5; and the claimed device having the termination resistance and a resistance automatic compensation circuit being connected, as recited in claim 6.

#### ***Conclusion***

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hung Vu whose telephone number is (571) 272-1666. The examiner can normally be reached on Tuesday to Friday 6:00-4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie C. Lee can be reached on (571) 272 - 1732. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Vu

October 28, 2005



Hung Vu

Primary Examiner